



Department of Electrical & Computer Engineering
ECE Lab Course Schedule - Fall 2021

August 23, 2021 - December 4, 2021

Sub	Cat	Sec	Description	Facil ID	Cap	Campus	Mtg Start	Mtg End	Mon	Tues	Wed	Thurs	Fri	Sat	Sun	Last Name	First Name
EEL	3712L	RVC	Logic Design I Lab	ONLINE	9999	OFF			N	N	N	N	N	N	N	O-Larnnithipong	Nonnarit
EEL	3712L	RXHA	Logic Design I Lab		0	OFF			N	N	N	N	N	N	N	Yen	Kang
EEL	3712L	RXHB	Logic Design I Lab		0	OFF			N	N	N	N	N	N	N	Yen	Kang
EEL	3712L	U01	Logic Design I Lab	EC2710	45	UPARK	6:30:00 PM	8:30:00 PM	Y	N	N	N	N	N	N	Newaz	AKM Iqtidar
EEL	3712L	U02	Logic Design I Lab	EC2710	45	UPARK	12:00:00 PM	2:00:00 PM	N	N	N	N	Y	N	N	Sonchan	Pontakorn
EEL	3712L	U03	Logic Design I Lab	EC2710	45	UPARK	3:00:00 PM	4:50:00 PM	Y	N	N	N	N	N	N	Newaz	AKM Iqtidar
EEL	3712L	U04	Logic Design I Lab	EC2710	45	UPARK	5:00:00 PM	7:00:00 PM	N	N	N	N	Y	N	N	Sonchan	Pontakorn
EEL	3712L	U05	Logic Design I Lab	EC2710	45	UPARK	6:30:00 PM	8:30:00 PM	N	N	Y	N	N	N	N	Sonchan	Pontakorn